CBM003 ADD/CHANGE FORM

1. Department: COSC  College: NSM
2. Faculty Contact Person: Shishir Shah  Telephone: 713-743-3360  Email: sshah@central.uh.edu
3. Course Information on New/Revised course:
   - Instructional Area / Course Number (*see CBM003 instructions) / Long Course Title:
     COSC / 3330 / Computer Architecture
   - Instructional Area / Course Number / Short Course Title (30 characters max.)
     COSC / 3330 / Computer Architecture
   - SCH: 3  Level: JR  CIP Code: 11.0701  Lect Hrs: 3  Lab Hrs: O  Term(s) Course is Offered (*see CBM003 instructions about selection): Fall
4. Justification for adding/changing course: To reflect change in prerequisite course
5. Was the proposed/revised course previously offered as a special topics course?  ☐ Yes  ☒ No
   If Yes, please complete:
   - Instructional Area / Course Number / Long Course Title:
   - Course ID: _______  Effective Date (currently active row): _______
6. Authorized Degree Program(s): B.S., Computer Science
   - Does this course affect major/minor requirements in the College/Department?  ☐ Yes  ☒ No
   - Does this course affect major/minor requirements in other Colleges/Departments?  ☐ Yes  ☒ No
   - Can the course be repeated for credit?  ☐ Yes  ☒ No (if yes, include in course description)
7. Grade Option: Letter (A, B, C, ...)  Instruction Type: lecture ONLY  (Note: Lect/Lab info. must match item 3, above. *See CBM003 instructions.)
8. If this form involves a change to an existing course, please obtain the following information from the course inventory: Instructional Area / Course Number / Long Course Title
   - COSC / 3330 / Computer Architecture
   - Course ID: 16808  Effective Date (currently active row): 8242009
9. Proposed Catalog Description: (If there are no prerequisites, type in "none").
   Cr: 3. (3-0).  Prerequisites: COSC 2410. COSC majors and minors only.  Description (30 words max.): Logic design, principles of operation of digital computers, and analysis of major components: arithmetic processing, memory, control and input/output units, instruction pipelining, SIMD and multiprocessor systems.
10. Dean's Signature: ____________________________  Date: 9/13/13  
    Print/Type Name: _______________