CBM003 ADD/CHANGE FORM

- Undergraduate Council [X]
- New Course [X]
- Course Change

Core Category: NONE Effective Fall 2007

- Graduate/Professional Studies Council
- New Course [ ]
- Course Change

Effective Fall __

1. Department: ECE
   College: ENGR

2. Person Submitting Form: E.J. Charleston
   Telephone: 713-7434490

3. Course Information on New/Revised course:
   - Instructional Area / Course Number / Long Course Title:
     ECE / 5346 / VLSI Design
   - Instructional Area / Course Number / Short Course Title (30 characters max.)
     ECE / 5346 / VLSI DESIGN
   - SCH: 3.00 Level: SR CIP Code: 1410010006 Lect Hrs: 3 Lab Hrs: 0

4. Justification for adding/changing course: To provide appropriate foundation for course

5. Was the proposed/revised course previously offered as a special topics course? [ ] Yes [X] No
   If Yes, please complete:
   - Instructional Area / Course Number / Long Course Title:
     ___/___/___
   - Content ID: ___ Start Date (yyyy3): ___

6. Is this course offered for undergraduate credit only? [ ] Yes [X] No

7. Authorized Degree Program(s): BSEE, BSCPE
   - Does this course affect major/minor requirements in the College/Department? [ ] Yes [X] No
   - Does this course affect major/minor requirements in other Colleges/Departments? [ ] Yes [X] No
   - Are special fees attached to this course? [ ] Yes [X] No
   - Can the course be repeated for credit? [ ] Yes [X] No

8. Grade Option: Letter (A, B, C...) Instruction Type: lecture

9. If this form involves a change to an existing course, please obtain the following information from the course inventory: Instructional Area / Course Number / Long Course Title
   ECE / 5346 / VLSI Design
   - Start Date (yyyy3): 20023 Content I.D.: 289736

10. Proposed Catalog Description: (If there are no prerequisites, type in "none".)
    Cr: 3 (3.0). Prerequisites: ECE 3456 or ECE 3457. Description (30 words max.): Integrated circuit design using computer-aided design methods; MOS, GaAs and bipolar techniques; standard cells, digital subcircuit and memory layout and design. Includes project requiring students to design and have fabricated a functional integrated circuit.

11. Dean’s Signature: __________________________ Date: 10/5/06

Print/Type Name: Dr. Fritz Claydon